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APPLICANTS Hidenori Morimoto, Kashihara, JAPAN;						
** CONTINUING DATA ***** <i>None, TL</i>						
** FOREIGN APPLICATIONS ***** JAPAN 2002-353732 12/05/2002 <i>yes, TL</i>						
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 03/08/2004						
Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <i>TL</i> Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 11	TOTAL CLAIMS 14	INDEPENDENT CLAIMS 3
ADDRESS 25226 MORRISON & FOERSTER LLP 755 PAGE MILL RD PALO ALTO, CA 94304-1018						
TITLE Semiconductor memory device and erase method for memory array						
FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:			<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		